

CLAIMS

1. A method, comprising:

determining a source pixel value of a first bit depth for a source pixel in a source image;

determining a destination pixel value of the first bit depth for a destination pixel in a destination image, the destination pixel corresponding to the source pixel;

deriving a blended pixel value of the first bit depth for a blended pixel in a blended image by applying an alpha value of a second bit depth to the source pixel value and the destination pixel value without converting the pixel values to the second bit depth; and

wherein the second bit depth is greater than the first bit depth.

2. The method as recited in claim 1, further comprising:

deriving an alpha dither matrix from a source dither matrix;

combining one or more alpha values associated with the source image with the alpha dither matrix to derive a source bitonal mask plane;

deriving a destination bitonal mask plane from the source bitonal mask plane; and

wherein the deriving the blended pixel value further comprises deriving the blended pixel value from the source pixel value, the source bitonal mask plane, the destination pixel value and the destination bitonal mask plane.

1 3. The method as recited in claim 2, wherein a stochastic technique is
2 used to derive the alpha dither matrix when the source printer matrix is
3 unknown.

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5 4. The method as recited in claim 2, wherein the alpha dither matrix
6 is derived from the source dither matrix by rotating the source dither matrix.

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8 5. The method as recited in claim 2, wherein the alpha dither matrix
9 is derived from the source dither matrix by performing a wraparound shift on
10 pixels in the source dither matrix.

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12 6. The method as recited in claim 2, wherein the alpha dither matrix
13 is derived from the source dither matrix by shifting pixels in the source dither
14 matrix a constant number of pixel positions.

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16 7. The method as recited in claim 6, wherein the constant number of
17 pixels is an odd number of pixels that is not a multiple of a dimension of the
18 source dither matrix, the dimension being height or width, depending on the
19 direction of the shift.

1 **8.** The method as recited in claim 2, wherein the one or more alpha
2 values associated with the source image is an alpha value that is constant for
3 each pixel in the source image.

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5 **9.** The method as recited in claim 2, wherein the one or more alpha
6 values associated with the source image are multiple alpha values from an
7 alpha channel that includes an alpha value for each pixel in the source image.

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9 **10.** The method as recited in claim 2, wherein the source bitonal
10 mask plane is inverted to derive the destination bitonal mask plane.

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12 **11.** The method as recited in claim 2, wherein deriving the blended
13 pixel value further comprises:

14 (a) logically applying a source bitonal mask plane pixel value from the
15 source bitonal mask plane to the source pixel value;

16 (b) logically applying a destination bitonal mask plane pixel value from
17 the destination bitonal mask plane to the destination pixel value; and

18 (c) logically combining the results of (a) and (b).

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20 **12.** The method as recited in claim 2, wherein deriving the blended
21 pixel value further comprises:

22 deriving a first intermediate value by performing a logical AND
23 operation with the source pixel value and the source bitonal mask plane;

24 deriving a second intermediate value by performing a logical AND
25 operation with the destination pixel value and the destination bitonal mask
plane; and

1 deriving the blended pixel value by performing a logical OR with the
2 first intermediate value and the second intermediate value.

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4 **13.** A computer-readable medium containing computer-executable
5 instructions that, when executed on a computer, perform the method recited in
6 claim 1.

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8 **14.** A printing device containing processor-executable instructions
9 that, when executed on a processor in the printing device, perform the method
10 recited in claim 1.

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12 **15.** An electronic appliance having a display and processor-
13 executable instructions that, when executed on a processor in the electronic
14 appliance, perform the method recited in claim 1.

1 **16.** A system, comprising:
2 memory;
3 a processor;
4 a source image stored in the memory, the source image consisting of
5 multiple source pixels, each source pixel having a source pixel value of a first
6 bit depth;
7 a destination image stored in the memory, the destination image
8 consisting of multiple destination pixels, each destination pixel having a
9 destination pixel value of the first bit depth;
10 one or more alpha values having a second bit depth stored in the
11 memory;
12 a source dither matrix stored in the memory;
13 an alpha dither matrix derivation module configured to derive an alpha
14 dither matrix from the source dither matrix;
15 a source mask plane derivation module configured to create a source
16 mask plane of the first depth from the one or more alpha values and the alpha
17 dither matrix;
18 a destination mask plane derivation module configured to create a
19 destination mask plane of the first depth from the source bitonal mask plane;
20 and
21 a blended image generator configured to create a blended image of the
22 first bit depth from the source image, the destination image, the source mask
23 plane and the destination mask plane, without converting the images to the
24 second bit depth.
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1 **17.** The system as recited in claim 16, wherein the first bit depth is
2 one bit and the second bit depth is eight bits.

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4 **18.** The system as recited in claim 16, further comprising a print
5 engine configured to print the blended bitonal image on a print medium.

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7 **19.** The system as recited in claim 16, further comprising an output
8 module configured to output the blended bitonal image to a video display.

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10 **20.** The system as recited in claim 16, wherein the one or more alpha
11 values further comprise multiple alpha values in an alpha channel.

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13 **21.** The system as recited in claim 16, wherein the destination mask
14 plane derivation module is further configured to create a destination mask
15 plane of the first depth by inverting the source bitonal mask plane.

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17 **22.** The system as recited in claim 16, wherein the blended image
18 generator is further configured to:

19 create a first intermediate value by logically applying the source mask
20 plane to the source image;

21 create a second intermediate value by logically applying the destination
22 mask plane to the destination image; and

23 create the blended image by logically combining the first intermediate
24 result with the second intermediate result.

1 **23.** A method for applying an 8-bit alpha channel to a bitonal source
2 image and a bitonal destination image to create a blended bitonal image, the
3 method comprising:

4 deriving an alpha dither matrix from a source dither matrix;
5 combining alpha values from the alpha channel with the alpha dither
6 matrix to derive a source bitonal mask plane;

7 inverting the source bitonal mask plane to derive a destination bitonal
8 mask plane;

9 applying the source bitonal mask plane to the bitonal source image to
10 derive a first intermediate value;

11 applying the destination bitonal mask plane to the bitonal destination
12 image to derive a second intermediate value;

13 logically adding the first intermediate value and the second intermediate
14 value to derive the blended bitonal image.

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16 **24.** The method as recited in claim 23, wherein deriving the alpha
17 dither matrix further comprises uniformly shifting pixels vertically in the
18 source dither matrix by a constant amount that is not a multiple of a height of
19 the source dither matrix.

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21 **25.** The method as recited in claim 23, wherein deriving the alpha
22 dither matrix further comprises uniformly shifting pixels horizontally in the
23 source dither matrix by a constant amount that is not a multiple of a width of
24 the source dither matrix.
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2 26. A printer configured to perform the method as recited in claim
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5 27. The method as recited in claim 23, wherein deriving the alpha
6 dither matrix further comprises uniformly shifting pixels horizontally and
7 vertically in the source dither matrix by a constant amount that is not a multiple
8 of a width or height of the source dither matrix.
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